

### Contingency Specification Rationale Worksheet

<b>WBS Level 4 Identifier: 54</b>		<b>Title: NCSX Facility Timing and Synchronization</b>		
<b>Originator: G. E. Oliaro</b>		<b>Date: 9/5/03</b>		
	<b>Technical</b>	<b>Schedule</b>	<b>Cost</b>	<b>Total</b>
<b>Risk Factor (Table 2-1):</b>	6	-	4	
<b>Weighting Factor (Table 2-2):</b>	1	1	2	
<b>Percent</b>	6	-	8	14
<b>Recommended Contingency Allowance (%): 14</b>				
<p><b>Rationale for Selection of Contingency Allowance:</b>            A new VME based clock design that has been used successfully on several High Energy Physics projects is being proposed. This is not a commercial product, however, documentation packages are claimed to be available for immediate manufacturing. The only drawback is that the design must be modified from a VME format to a CompactPCI and PCI formats. Also device drivers for UNIX, Windows and LabView must be written in order to make this product the universal timing solution for NCSX.</p>				