

# Central Controls and Computing WBS5

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WBS5 Work Package Manager





### **Agenda**



- Introduction
- Requirements and Interfaces
- Cost and schedule
- Risks and mitigation
- Responses to past review recommendations





#### Introduction



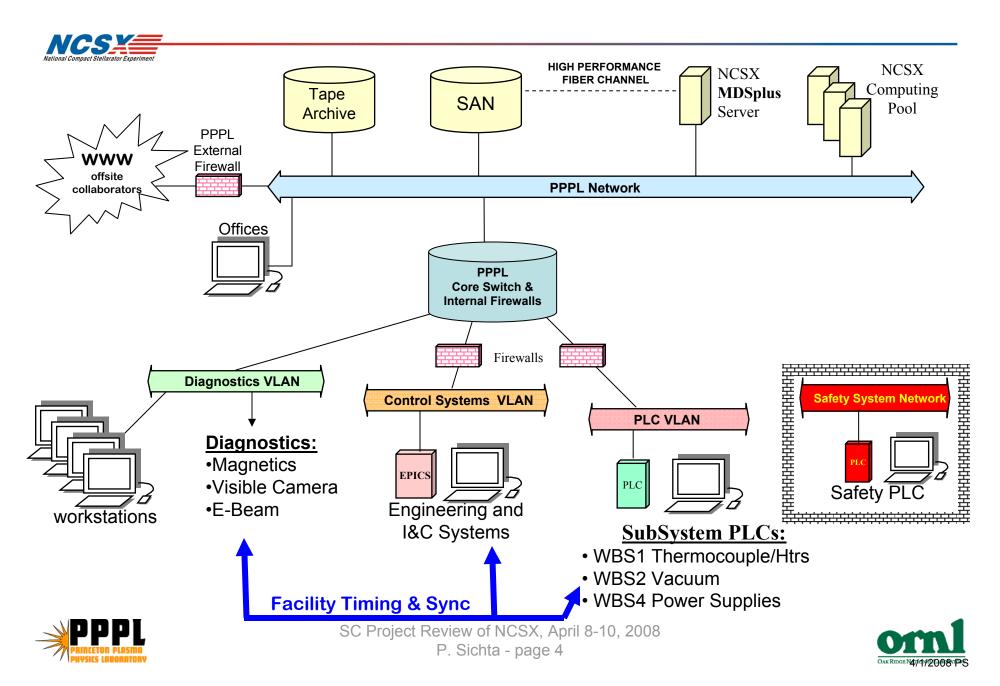
Central Controls and Computing will provide the equipment and services to support: 1) integrated and remote control; 2) data acquisition, analysis, and storage; 3) facility timing and synchronization; 4) central safety and interlocks.

- Network and Fiber Optic Infrastructure (WBS 51)
- Central Instrumentation and Control (WBS 52)
- Data Acquisition and Facility Computing (WBS 53)
- Facility Timing and Synchronization (WBS 54)
- Real-Time Plasma and Power Supply Control (WBS 55)
- Central Safety and Interlock System (WBS 56)
- Management and Integration (WBS 58)





## **NCSX Computing Overview**



#### Requirements



- An NCSX System Design Description (SDD) was written in 2003, before CD-2. The primary elements of that design remain intact.
- My current estimate is derived from the SDD, ongoing technical discussions and design reviews, and recent experience with similar systems on NSTX.
- A WBS5 System Requirements Document (SRD,BSPEC)
  will be reviewed and approved prior to the *Preliminary*Design Review for each WBS5 element.
- Design Complexity & Maturity
  - Many of the technologies for WBS5/NCSX are currently in use on NSTX, so complexity is low for our experienced staff.
  - The current workscope has completed neither Preliminary nor Final design, so the maturity is medium.





#### **CD-4 Interface List**



WBS51 Network & Fiber Optic	WBS1 Thermocouple/Heater Local I&C WBS2 Vacuum/Fueling Systems WBS3 Diagnostics WBS4 Power Systems
WBS52 Central I&C	WBS1 Thermocouple/Heater Local I&C WBS2 Vacuum/Fueling Systems WBS4 Power Systems
WBS53 Data Acquisition and Management	WBS1 Thermocouple Local I&C WBS2 Vacuum/Fueling Systems WBS3 Diagnostics WBS4 Power Systems
WBS54 Timing & Synchronization	WBS3 Diagnostics WBS4 Power Systems
WBS55 Real-Time Control	WBS2 Vacuum/Fueling Systems WBS4 Power Supply Control
WBS56 Central Safety and Interlocks	Access Control: WBS4 Power System Areas, WBS7 Test Cell. SubSystem Interlocks: WBS4 Power Systems. NCSX (Global) E-Stop.





#### **Basis of Estimate**



#### Labor:

- referenced actual engineering hours from FY97-99 for the NSTX first plasma.
- experience with similar activities for NSTX.
- 'expert' estimates (e.g. Erik Perry).

#### M&S

- recent purchase of parts for NSTX and other lab infrastructure projects.
- catalog prices.
- includes spares and service contracts.
- selective use of NSTX equipment.





#### **WBS5 Aggregate Cost**



#### Reference WAFs for labor and M&S detail for WBS51-58.

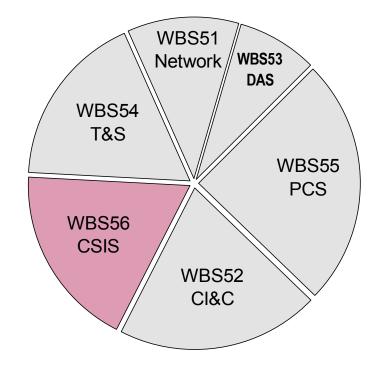
http://ncsx.pppl.gov/Rebaseline/Rebaseline\_index.htm

#### WBS5 ETC = \$2.1 M

Softwr/Elec Engineering: **3.6 years** 

Elec/Mech/Draft Tech: 3.1 years

'Materials & Services': \$432 K





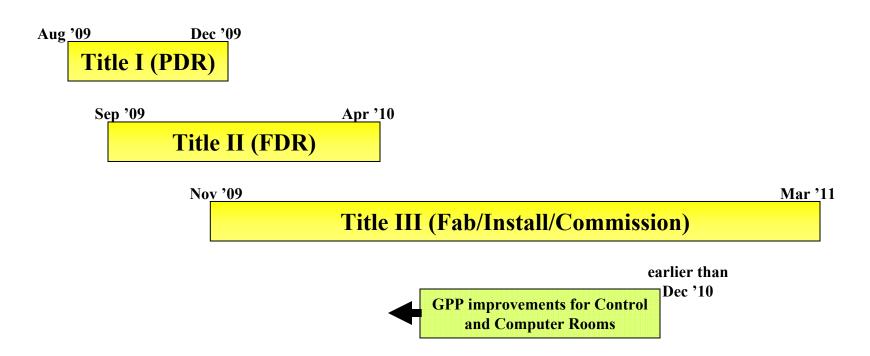


#### WBS51-WBS56 Aggregate Schedule



Reference *Resource Loaded Schedule* pages 53-56 for schedule detail for WBS51 – WBS58.

http://ncsx.pppl.gov//Reviews/FY08/BCP\_2008/Docs/NCSX\_RLS0403.pdf







# **Schedule (51,52)**



ID	MILE -STONE LEVEL	Activity Description	Duration SI (work days	Start	Forecast Finish	Total Float	Cost to Complete from 2/1/08	FY08 FY09 FY10 FY11 FY12 F
453-1-5	Fr Cc	vē Tast		27.J.N11	224UG11	11	18,965,06	41=015k; EA/88 =05h ■■ER/HW = 2h; EE/8#=
453-1-6	Trim (	Doll Goll Test	40	27JUN11		11	136,368.68	EESTB =54hr: 41=01\$k: EAUSB =08hr
								EE/EM = 32 hr; EE //S M = EE//T B = 54 hr;
453-4-8	Testin	ng PTPs, ISTFs	40	27JIJN 11		11	159,275.76	41-106k   BE/Eff4-240by;   FE/48M-330by; FE/1F8-376by;
- Network	and Fiber In	nfrastructure						
		Fiber Infrastruct-SICHTA						
R51-10	Prelim	ninary Design	30	01OCT09*	11NOV09	265	8,977.30	
R51-11	PDR	may besign	0	0700709	11NOV09	265	0.00	EC//EM =40 hr ; ec//tb=10;ea//sb=20
R51-20		Design	60	12NOV09	17FEB10	265	11,919.00	EC//EM =50hr ;ec//tb=30;ea//sb=20
R51-21	FDR		0	12.70733	17FEB10	265	0.00	EO/EM -30H ,80/10-30,86/85-20
R51-30	Procu	rement	60	18JUN10*	13SEP10	180	95,270.68	EC//EM =24hr; ec//tb=12
R51-50	Install	lation	80	14SEP10	13JAN11	180	97,809.22	41=68\$k; 
		124011					7,390.48	EA//SB =240hr; EM//TB =570hr; ee
R51-60	Test		14	14JAN11	02FEB11	180		EC//EM =28hr; EC//TB =40 :
		ion & Control s-SICHTA		1707 0411	027 2377	700	7,330.40	UEC//EM -2011, EC//10 -40 ;
ob: 5201 - I	&C Systems	s-SICHTA						
ob: <b>5201 - I</b> R52-10	Rec Systems		20	03AUG09*	28AUG09	218	9,847.00	
ob: 5201 - I R52-10 R52-11	Prelim	ninary Design-Infrastructure	20 0	03AUG09*	28AUG09 28AUG09	218 218	9,847.00	□EC//EM =60hr;ec//tb=20
R52-10 R52-11 R52-20	Prelim PDR Final I	s-SICHTA	20 0 45		28AUG09 28AUG09 02NOV09	218 218 218	9,847.00 0.00 20,115.35	
ob: 5201 - I R52-10 R52-11	Prelim PDR Final I	ninary Design-Infrastructure  Design-Infrastructure	20 0	03AUG09* 31AUG09	28AUG09 28AUG09 02NOV09 02NOV09	218 218 218 218 218	9,847.00 0.00 20,115.35 0.00	☐EC//EM =60hr;ec//tb=20 ☐EC//EM =100hr;ec//tb=80
R52-10 R52-11 R52-20 R52-21	Prelim PDR Final I FDR Prelim	ninary Design-Infrastructure	20 0 45	03AUG09*	28AUG09 28AUG09 02NOV09	218 218 218	9,847.00 0.00 20,115.35	EC//EM =60hr;ec//tb=20  EC//EM =100hr;ec//tb=80
R52-10 R52-11 R52-20 R52-21 R52-25	Prelim PDR Final I FDR Prelim FINAL FINAL FINAL FINAL	Design-Infrastructure  Design-Infrastructure	20 0 45 0	03AUG09* 31AUG09 03NOV09*	28AUG09 28AUG09 02NOV09 02NOV09 25JAN10	218 218 218 218 218 256	9,847.00 0.00 20,115.35 0.00 5,754.80	EC//EM =60hr;ec//tb=20  EC//EM =100hr;ec//tb=30  EC//EM =40  EC//EM =140hr;ec//tb=20
R52-10 R52-11 R52-20 R52-21 R52-25 R52-27 R52-30	Prelim PDR Final I FDR Prelim Fral I Fral I Fral I Fral I	Design-Infrastructure  Design-Infrastructure  Design-Subsystems  Design-Subsystems  Design-Subsystems	20 0 45 0 50 50	03AUG09* 31AUG09 03NOV09* 26JAN10 03NOV09	28AUG09 28AUG09 02NOV09 02NOV09 25JAN10 05APR10 15FEB10	218 218 218 218 218 256 256 291	9,847.00 0.00 20,115.35 0.00 5,754.80 21,644.80 100,681.80	EC//EM =60hr;ec//lb=20  EC//EM =100hr;ec//lb=80  EC//EM =40  EC//EM =40hr;ec//lb=20  EC//EM =40hr;
R52-10 R52-11 R52-21 R52-21 R52-25 R52-27	Prelim PDR Final I FDR Prelim FOR Prelim FINAL F	Design-Infrastructure  Design-Infrastructure  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems	20 0 45 0 50	03AUG09* 31AUG09 03NOV09* 26JAN10 03NOV09 03NOV09*	28AUG09 28AUG09 02NOV09 02NOV09 25JAN10 05APR10 15FEB10 11JAN10	218 218 218 218 218 256 256 291 218	9,847.00 0.00 20,115.35 0.00 5,754.80 21,644.80 100,681.80	EC//EM =60hr;ec//lb=20  EC//EM =100hr;ec//tb=80  EC//EM =440  EC//EM =440hr;ec//tb=20  EC//EM =40hr;  H=7.1\$k;  EC//EM =80hr;
R52-10 R52-11 R52-20 R52-21 R52-25 R52-27 R52-30 R52-40	Prelim PDR Final I FDR Prelim FOR Prelim FINAL F	Design-Infrastructure  Design-Infrastructure  Design-Subsystems  Design-Subsystems  Design-Subsystems	20 0 45 0 50 50 65	03AUG09* 31AUG09 03NOV09* 26JAN10 03NOV09	28AUG09 28AUG09 02NOV09 02NOV09 25JAN10 05APR10 15FEB10	218 218 218 218 256 256 291 218 406	9,847.00 0.00 20,115.35 0.00 5,754.80 21,644.80 100,681.80	EC//EM =60hr;ec//tb=20  EC//EM =100hr;ec//tb=80  EC//EM =440  EC//EM =440hr;ec//tb=20  EC//EM =40hr;  EC//EM =80hr;
R52-10 R52-11 R52-20 R52-21 R52-25 R52-27 R52-30 R52-40 R52-50	Prelim PDR Final I FDR Prelim FOR Prelim Final I FINAL	Design-Infrastructure  Design-Infrastructure  Design-Subsystems Design-Subsystems Design-Subsystems Design-Subsystems Design-Subsystems Design-Subsystems Design-Subsystems	20 0 45 0 50 50 65 40	03AUG09*  31AUG09  03NOV09*  26JAN10  03NOV09*  03NOV09*  03NOV09*	28AUG09 28AUG09 02NOV09 02NOV09 25JAN10 05APR10 15FEB10 11JAN10	218 218 218 218 218 256 256 291 218	9,847.00 0.00 20,115.35 0.00 5,754.80 21,644.80 100,681.80 11,509.60 5,754.80	EC//EM =60hr;ec//lb=20  EC//EM =100hr;ec//tb=80  EC//EM =440  EC//EM =440hr;ec//tb=20  EC//EM =40hr;  EC//EM =40hr;  EC//EM =40hr;
R52-10 R52-11 R52-20 R52-21 R52-25 R52-27 R52-30 R52-40 R52-60 R52-70	Prelim PDR Final I FDR Prelim Final I FINAL I FOCULO FOCUL	Design-Infrastructure  Design-Infrastructure  Design-Subsystems Design-Infrastructure	20 0 45 0 50 50 65 40 40 40	03AUG09*  31AUG09  03NOV09*  26JAN10  03NOV09*  03NOV09*  03NOV09*  12JAN10	28AUG09 28AUG09 02NOV09 02NOV09 25JAN10 05APR10 15FEB10 11JAN10 11JAN10 08MAR10	218 218 218 218 228 256 256 291 218 406 406 218	9,847.00 0.00 20,115.35 0.00 5,754.80 21,644.80 100,681.80 11,509.60 5,754.80 17,264.40 25,507.20	EC//EM =60hr;ec//tb=20  EC//EM =100hr;ec//tb=80  EC//EM =40  EC//EM =140hr;ec//tb=20  EC//EM =40hr;  HEC//EM =30hr;  EC//EM =40hr;  EC//EM =160hr;  EC//EM =160hr;  35=02\$k;
R52-10 R52-11 R52-20 R52-21 R52-25 R52-27 R52-30 R52-40 R52-50 R52-60 R52-60 R52-80	Prelim PDR Final I FDR Prelim Final I Procu EPICS EPICS IOC P OPC - Appl.	Design-Infrastructure  Design-Infrastructure  Design-Subsystems Design-Infrastructure	20 0 45 0 50 50 65 40 40 40 40	03AUG09*  31AUG09  03NOV09*  26JAN10  03NOV09*  03NOV09*  03NOV09*  12JAN10  09MAR10	28AUG09 28AUG09 02NOV09 02NOV09 25JAN10 05APR10 15FEB10 11JAN10 11JAN10 08MAR10 05OCT10	218 218 218 218 228 256 256 291 218 406 406 218 218	9,847.00 0.00 20,115.35 0.00 5,754.80 21,644.80 100,681.80 11,509.60 5,754.80 17,264.40 25,507.20	EC//EM =60hr;ec//tb=20  EC//EM =100hr;ec//tb=30  EC//EM =40  EC//EM =40hr; 41=715k;  EC//EM =30hr; EC//EM =40hr; EC//EM =120hr; EC//EM =160hr; 35=02\$k;  EC//EM =160hr;
R52-10 R52-11 R52-20 R52-21 R52-25 R52-27 R52-30 R52-40 R52-60 R52-70	Prelim PDR Final I FDR Prelim Final I Procu EPICS EPICS IOC P OPC - Appl.	Design-Infrastructure  Design-Infrastructure  Design-Subsystems Design-Subsystems Design-Subsystems Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Infrastructure	20 0 45 0 50 50 65 40 40 40	03AUG09*  31AUG09  03NOV09*  26JAN10  03NOV09*  03NOV09*  03NOV09*  12JAN10  09MAR10  09MAR10	28AUG09 28AUG09 02NOV09 02NOV09 25JAN10 05APR10 15FEB10 11JAN10 11JAN10 08MAR10 05OCT10 14JUL10	218 218 218 218 228 256 256 291 218 406 406 218 218 276	9,847.00 0.00 20,115.35 0.00 5,754.80 21,644.80 100,681.80 11,509.60 5,754.80 17,264.40 25,507.20 35,716.41 20,141.80	EC//EM =60hr;ec//tb=20  EC//EM =100hr;ec//tb=80  EC//EM =40  EC//EM =140hr;ec//tb=20  EC//EM =40hr;  EC//EM =80hr;  EC//EM =40hr;  EC//EM =160hr;  EC//EM =160hr;  EC//EM =160hr;  EC//EM =140hr;
R52-10 R52-11 R52-20 R52-21 R52-25 R52-27 R52-30 R52-40 R52-50 R52-60 R52-70 R52-80 R52-90	Prelim PDR Final I FDR Prelim Final I Procu EPICS EPICS IOC P OPC - Appl. Progr.	Design-Infrastructure  Design-Infrastructure  Design-Subsystems Design-Subsystems Design-Subsystems Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Subsystems  Design-Infrastructure	20 0 45 0 50 50 65 40 40 40 40 148	03AUG09*  31AUG09  03NOV09*  26JAN10  03NOV09*  03NOV09*  03NOV09*  12JAN10  09MAR10	28AUG09 28AUG09 02NOV09 02NOV09 25JAN10 05APR10 15FEB10 11JAN10 11JAN10 08MAR10 05OCT10	218 218 218 218 228 256 256 291 218 406 406 218 218	9,847.00 0.00 20,115.35 0.00 5,754.80 21,644.80 100,681.80 11,509.60 5,754.80 17,264.40 25,507.20	EC//EM =60hr;ec//lb=20  EC//EM =100hr;ec//lb=80  EC//EM =40  EC//EM =40hr;ec//lb=20  EC//EM =40hr; H=71\$k; EC//EM =80hr; EC//EM =40hr; EC//EM =160hr; EC//EM =248hr;





# **Schedule (53,54)**

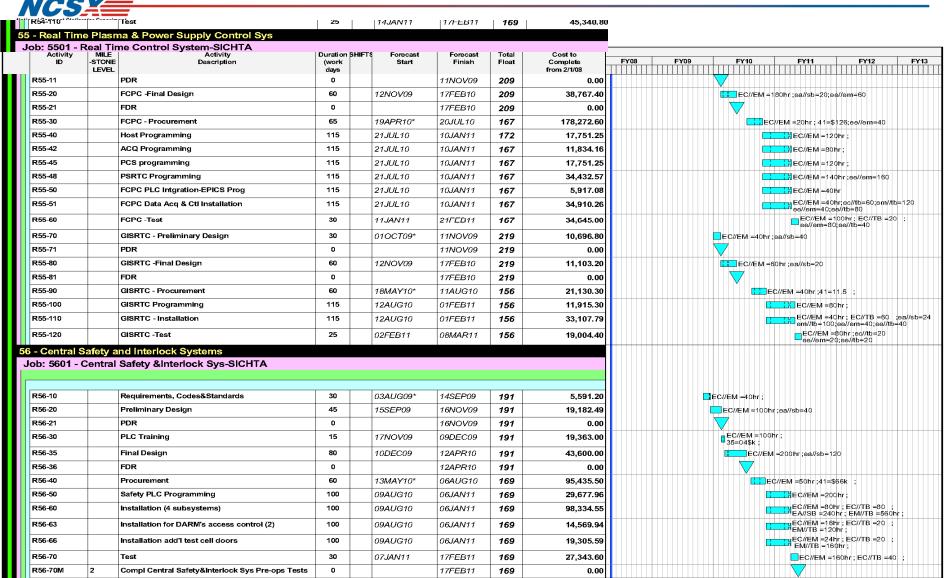


ID	MILE Activity -STONE Description LEVEL	Duration S (work days	HIFTS Forecast Start	Forecast Finish	Total Float	Cost to Complete from 2/1/08	FY08 FY09 FY10 FY11 FY12 F
- Data Aco	quisition & Facility Computing						
ob: 5301 -	Data Acquisition-SICHTA						
R53-10	Preliminary Design	30	03AUG09*	14SEP09	182	5,591.20	□ EC//EM =40hr;
R53-11	PDR	0		14SEP09	182	0.00	
R53-20	Final Design	30	15SEP09	26OCT09	182	11,378.72	EC//EM =80hr;
R53-21	FDR	0		26OCT09	182	0.00	
R53-30	Procurement	30	27OCT09	09DEC09	182	32,291.40	EC//EM =20hr; 37=02 ; 41=\$22k
R53-40	Installation	30	10DEC09	01FEB10	182	3,006.00	₩ EC//TB =40 ;
R53-50	MDSplus Installation	20	02FEB10	01MAR10	182	11,509.60	DEC//EM =80hr;
R53-60	MDSplus Programming - Tree Design	20	02MAR10	29MAR10	182	11,509.60	□EC//EM ≠80hr;
R53-70	MDSplus Programming - Shot Sync	20	30MAR10	26APR10	182	11,509.60	EC//EM =80hr;
R53-100	Applications Support (3 Diags)	60	27APR10	21JUL10	182	8,632.20	EC//EM =60hr;
R53-110	Programming - Misc.	60	27APR10	21JUL10	182	23,019.20	EC//EM =160hr;
R53-80	MDSplus Programming - Dispatcher	60	23AUG10*	15NOV10	160	23,641.28	EC//EM =160 hr;
R53-90	MDSplus Programming - Acquisition	55	16NOV10	10FEB11	160	12,092.80	<b>C</b> =EC//EM =80hr;
R53-120	Test	14	11FEB11	02MAR11	160	12,227.60	EC//EM =60hr; EC//TB =40 ;
DD: 0-101	Facility Timing & SynchronSICHTA						
OD. 0-101 -	racinty rinning & Synchron. Sicrita						
		30	02NOV09*	15DEC09	202	11 403 80	
R54-10	Preliminary System Design	30	02NOV09*	15DEC09	202	11,403.80	EC//EM =60hr ;ec//tb=4;ea//sb=20
R54-10 R54-11	Preliminary System Design PDR	0		15DEC09	202	0.00	
R54-10 R54-11 R54-20	Preliminary System Design PDR Final SystemDesign		02NOV09* 16DEC09	15DEC09 19FEB10	202	0.00 17,052.80	EC//EM =60hr;ec//tb=4;ea//sb=20
R54-10 R54-11	Preliminary System Design PDR Final SystemDesign FDR	0 40	16DEC09	15DEC09 19FEB10 19FEB10	202 202 302	0.00 17,052.80 0.00	EC//EM =80hr;ec//tb=8;ea//\$b=40
R54-10 R54-11 R54-20 R54-21	Preliminary System Design PDR Final SystemDesign	0 40 0		15DEC09 19FEB10	202	0.00 17,052.80	
R54-10 R54-11 R54-20 R54-21 R54-30	Preliminary System Design PDR Final SystemDesign FDR Preliminary Design - Clock Dist.	0 40 0 20	16DEC09	15DEC09 19FEB10 19FEB10 19MAR10	202 202 302 302	0.00 17,052.80 0.00 15,311.10	EC//EM =80hr;ec//tb=8;ea//sb=40    EC//EM =20hr; EE//EM =70hr;ec//tb=4   EC//EM =20hr; EE//EM =128hr;ec//tb=8
R54-10 R54-11 R54-20 R54-21 R54-30 R54-40	Preliminary System Design  PDR  Final SystemDesign  FDR  Preliminary Design - Clock Dist.  Final Design - Clock Dist.	0 40 0 20 30	16DEC09 22FEB10 22MAR10	15DEC09 19FEB10 19FEB10 19MAR10 30APR10	202 202 302 302 302 302 262	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08	EC//EM =80hr;ec//.tb=8;ea//sb=40    EC//EM =20hr; EE//EM =70hr;ec//tb=4   EC//EM =20hr; EE//EM =128hr;ec//tb=8   EC//EM =24hr; EE//EM =120hr;   EE//TB =200hr;ec//tb=24
R54-10 R54-11 R54-20 R54-21 R54-30 R54-40 R54-50 R54-60	Preliminary System Design PDR Final SystemDesign FDR Preliminary Design - Clock Dist. Final Design - Clock Dist. Test - Clock Dist. Procurement	0 40 0 20 30 40	16DEC09  22FEB10  22MAR10  29JUN10  22FEB10*	15DEC09 19FEB10 19FEB10 19MAR10 30APR10 24AUG10 28JUN10	202 202 302 302 302 302 262 212	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08	EC//EM =80hr;ed/.tb=8;ea//sb=40    EC//EM =20hr; EE//EM =70hr;ed//tb=4   EC//EM =20hr; EE//EM =128hr;ed//tb=8   EC//EM =24hr; EE//EM =120hr;   EC//EM =20hr;ed//tb=24   EC//EM =44hr;
R54-10 R54-11 R54-20 R54-21 R54-30 R54-40 R54-50	Preliminary System Design PDR Final SystemDesign FDR Preliminary Design - Clock Dist. Final Design - Clock Dist. Test - Clock Dist. Procurement UNT - Timing & Seq Emulation (FPGA Pgm)	0 40 0 20 30 40 90	16DEC09  22FEB10 22MAR10 29JUN10 22FEB10* 16DEC09*	15DEC09 19FEB10 19FEB10 19MAR10 30APR10 24AUG10 28JUN10 30APR10	202 202 302 302 302 302 262 212 342	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08 101,257.28 14,901.40	EC//EM =20hr; ec//tb=8;ea//sb=40    EC//EM =20hr; EE//EM =70hr;ec//tb=4   EC//EM =20hr; EE//EM =128hr;ec//tb=8   EC//EM =24hr; EE//EM =120hr;   EC//EM =24hr; EE//EM =4120hr;   EC//EM =44hr;   EC//EM =24hr; EC//TB =160;
R54-10 R54-11 R54-21 R54-21 R54-30 R54-40 R54-50 R54-60 R54-70	Preliminary System Design PDR Final SystemDesign FDR Preliminary Design - Clock Dist. Final Design - Clock Dist. Test - Clock Dist. Procurement	0 40 0 20 30 40	16DEC09  22FEB10  22MAR10  29JUN10  22FEB10*	15DEC09 19FEB10 19FEB10 19MAR10 30APR10 24AUG10 28JUN10	202 202 302 302 302 262 212 342 202	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08 101,257.28 14,901.40 23,058.08	EC//EM =80hr;ed/.tb=8;ea//sb=40    EC//EM =20hr; EE//EM =70hr;ed//tb=4   EC//EM =20hr; EE//EM =128hr;ed//tb=8   EC//EM =24hr; EE//EM =120hr;   EE//EB =44hr;   EC//EM =24hr; EE//TB =160;   EC//EM =20hr;ed//tb=24
R54-10 R54-11 R54-20 R54-21 R54-30 R54-40 R54-50 R54-60 R54-70 R54-80	Preliminary System Design PDR Final SystemDesign FDR Preliminary Design - Clock Dist. Final Design - Clock Dist. Test - Clock Dist. Procurement UNT - Timing & Seq Emulation (FPGA Pgm) UNT - Device Driver Prog (EPICS/MDSplus)	0 40 0 20 30 40 90 90	16DEC09  22FEB10 22MAR10 29JUN10 22FEB10* 16DEC09* 19APR10	15DEC09 19FEB10 19FEB10 19MAR10 30APR10 24AUG10 28JUN10 30APR10 06OCT10	202 202 302 302 302 302 262 212 342	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08 101,257.28 14,901.40	EC//EM =80hr;ec//tb=8;ea//\$b=40  EC//EM =20hr; EE//EM =70hr;ec//tb=4  EC//EM =20hr; EE//EM =128hr;ec//tb=8  EC//EM =24hr; EE//EM =120hr; EC//EM =44hr;  41=\$71k  EC//EM =20hr;EC//TB =160;  EC//EM =20hr;EC//TB =160;
R54-10 R54-11 R54-20 R54-21 R54-30 R54-40 R54-50 R54-60 R54-70 R54-90 R54-100	Preliminary System Design  PDR  Final SystemDesign  FDR  Preliminary Design - Clock Dist.  Final Design - Clock Dist.  Test - Clock Dist.  Procurement  UNT - Timing & Seq Emulation (FPGA Pgm)  UNT - Device Driver Prog (EPICS/MDSplus)  Central Clock (EPICS) Programming	0 40 0 20 30 40 90 90 120 30 90	16DEC09  22FEB10 22MAR10 29JUN10 22FEB10* 16DEC09* 19APR10 07OCT10 30AUG10*	15DEC09 19FEB10 19FEB10 19MAR10 30APR10 24AUG10 28JUN10 30APR10 06OCT10 17NOV10	202 202 302 302 302 262 212 342 202 202 169	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08 101,257.28 14,901.40 23,058.08 12,092.80 50,074.31	EC//EM =80hr;ec//.tb=8;ea//sb=40  EC//EM =20hr; EE//EM =70hr;ec//tb=4  EC//EM =20hr; EE//EM =128hr;ec//tb=8  EC//EM =24hr; EE//EM =120hr; EE//TB =200hr;ec//tb=24  EC//EM =20hr;ec//tb=24  EC//EM =20hr;ec//tb=26  EC//EM =50hr;EC//TB =160 ;  EC//EM =80hr; EC//EM =56hr;EA//SB =40hr; EC//TB =80 ;EM//TB =340hr;
R54-10 R54-11 R54-20 R54-21 R54-30 R54-40 R54-50 R54-60 R54-70 R54-80 R54-90 R54-100 R54-110	Preliminary System Design PDR Final SystemDesign FDR Preliminary Design - Clock Dist. Final Design - Clock Dist. Test - Clock Dist. Procurement UNT - Timing & Seq Emulation (FPGA Pgm) UNT - Device Driver Prog (EPICS/MDSplus) Central Clock (EPICS) Programming Installation Test	0 40 0 20 30 40 90 90 120 30	16DEC09  22FEB10 22MAR10 29JUN10 22FEB10* 16DEC09* 19APR10 07OCT10	15DEC09 19FEB10 19FEB10 19FAR10 30APR10 24AUG10 28JUN10 30APR10 06OCT10 17NOV10	202 202 302 302 302 262 212 342 202 202	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08 101,257.28 14,901.40 23,058.08 12,092.80	EC//EM =80hr;ec//.tb=8;ea//sb=40  EC//EM =20hr; EE//EM =70hr;ec//tb=4  EC//EM =20hr; EE//EM =128hr;ec//tb=8  EC//EM =24hr; EE//EM =120hr; EE//TB =200hr;ec//tb=24  EC//EM =20hr;ec//tb=24  EC//EM =20hr;ec//tb=26  EC//EM =50hr;EC//TB =160 ;  EC//EM =80hr; EC//EM =56hr;EA//SB =40hr; EC//TB =80 ;EM//TB =340hr;
R54-10 R54-11 R54-20 R54-21 R54-30 R54-40 R54-50 R54-60 R54-60 R54-70 R54-80 R54-90 R54-100 R54-110	Preliminary System Design PDR Final SystemDesign FDR Preliminary Design - Clock Dist. Final Design - Clock Dist. Test - Clock Dist. Procurement UNT - Timing & Seq Emulation (FPGA Pgm) UNT - Device Driver Prog (EPICS/MDSplus) Central Clock (EPICS) Programming Installation Test Re Plasma & Power Supply Control Sys	0 40 0 20 30 40 90 90 120 30 90	16DEC09  22FEB10 22MAR10 29JUN10 22FEB10* 16DEC09* 19APR10 07OCT10 30AUG10*	15DEC09 19FEB10 19FEB10 19MAR10 30APR10 24AUG10 28JUN10 30APR10 06OCT10 17NOV10	202 202 302 302 302 262 212 342 202 202 169	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08 101,257.28 14,901.40 23,058.08 12,092.80 50,074.31	EC//EM =80hr;ec//.tb=8;ea//sb=40  EC//EM =20hr; EE//EM =70hr;ec//tb=4  EC//EM =20hr; EE//EM =128hr;ec//tb=8  EC//EM =24hr; EE//EM =120hr; EE//FB =200hr;ec//tb=24  EC//EM =24hr; EC//EM =44hr;  EC//EM =44hr;  EC//EM =44hr; EC//EM =44hr; EC//EM =44hr; EC//EM =44hr; EC//EM =56hr;EA//SB =40hr; EC//EM =80hr;EA//SB =40hr; EC//EM =80hr;EA//SB =40hr;
R54-10 R54-11 R54-20 R54-21 R54-30 R54-40 R54-50 R54-60 R54-60 R54-70 R54-80 R54-90 R54-100 R54-110	Preliminary System Design PDR Final SystemDesign FDR Preliminary Design - Clock Dist. Final Design - Clock Dist. Test - Clock Dist. Procurement UNT - Timing & Seq Emulation (FPGA Pgm) UNT - Device Driver Prog (EPICS/MDSplus) Central Clock (EPICS) Programming Installation Test	0 40 0 20 30 40 90 90 120 30 90	16DEC09  22FEB10 22MAR10 29JUN10 22FEB10* 16DEC09* 19APR10 07OCT10 30AUG10*	15DEC09 19FEB10 19FEB10 19MAR10 30APR10 24AUG10 28JUN10 30APR10 06OCT10 17NOV10	202 202 302 302 302 262 212 342 202 202 169	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08 101,257.28 14,901.40 23,058.08 12,092.80 50,074.31	EC//EM =80hr;ec//.tb=8;ea//sb=40  EC//EM =20hr; EE//EM =70hr;ec//tb=4  EC//EM =20hr; EE//EM =128hr;ec//tb=8  EC//EM =24hr; EE//EM =120hr; EE//FB =200hr;ec//tb=24  EC//EM =24hr; EC//EM =44hr;  EC//EM =44hr;  EC//EM =44hr; EC//EM =44hr; EC//EM =44hr; EC//EM =44hr; EC//EM =56hr;EA//SB =40hr; EC//EM =80hr;EA//SB =40hr; EC//EM =80hr;EA//SB =40hr;
R54-10 R54-11 R54-20 R54-21 R54-30 R54-40 R54-50 R54-60 R54-60 R54-70 R54-80 R54-90 R54-100 R54-110	Preliminary System Design PDR Final SystemDesign FDR Preliminary Design - Clock Dist. Final Design - Clock Dist. Test - Clock Dist. Procurement UNT - Timing & Seq Emulation (FPGA Pgm) UNT - Device Driver Prog (EPICS/MDSplus) Central Clock (EPICS) Programming Installation Test Re Plasma & Power Supply Control Sys	0 40 0 20 30 40 90 90 120 30 90	16DEC09  22FEB10 22MAR10 29JUN10 22FEB10* 16DEC09* 19APR10 07OCT10 30AUG10*	15DEC09 19FEB10 19FEB10 19MAR10 30APR10 24AUG10 28JUN10 30APR10 06OCT10 17NOV10	202 202 302 302 302 262 212 342 202 202 169	0.00 17,052.80 0.00 15,311.10 25,664.84 42,142.08 101,257.28 14,901.40 23,058.08 12,092.80 50,074.31	EC//EM =80hr;ec//tb=8;ea//sb=40   EC//EM =20hr; EE//EM =70hr;ec//tb=4   EC//EM =20hr; EE//EM =128hr;ec//tb=8   EC//EM =24hr; EE//EM =120hr;   EC//EM =44hr;   EC//EM =50hr;ec//tb=24   EC//EM =50hr;ec//tB=160;





#### **Schedule (55,56)**







# Schedule (58)



Activity ID	MILE -STONE LEVEL	Activity Description	Duration (work days	SHIFTS	Forecast Start	Forecast Finish	Total Float	Cost to Complete from 2/1/08	FY08	FY09	FY10	FY11	FY12	FY13
- Central		gement and Integration												
		&C Integr& Oversight-SICHTA												
R58-20	W	VBS58 -FY08 Management & Integration LOE	250*		01OCT07A	30SEP08	1,521	14,454.84		ec#em=160				
R58-20 R58-30		WBS58 -FY08 Management & Integration LOE WBS58 -FY09 Management & Integration LOE	250* 249		01OCT07A 01OCT08*	30SEP08 30SEP09	1,521 1,272	14,454.84 16,773.60		ec//em=160	ec//em=120			
	w						+ - +				ec//em=120	ec//em=120		





### **Risks and Mitigation**



# Reference NCSX Risk Register (page 2, item 'e') for WBS5 risks.

http://ncsx.pppl.gov//Reviews/FY08/BCP\_2008/Docs/RR\_Rev28a.pdf

Risk Description	Mitigation Plan	Likelihood	Consequence	Risk Ranking
Loss of staff with experience in specialized software will delay availability of Central I&C system.	Staff have recently been brought on board in anticipation of growing NCSX I&C needs. The planned shutdown of NSTX after FY10 will increase the availability of similar resources for NCSX.	VU	Marginal	Low





#### Response to Past Review Findings



# 1. Work with ES&H on Safety System Requirements and design basis.

• PPPL's *ES&H Directives Manual, section 2-5* "Personnel and Safety Interlock Systems" is in the process of being updated.

#### 2. Document Basis of Estimate

- A WBS5 notebook has been prepared to compile the design basis.
  - Copies of recent requisitions for similar equipment.
  - Catalog cut-sheets with prices.
  - Actual NSTX engineering-hours (labor) tabulation for first plasma.





#### Conclusion



The NCSX central controls and computing are similar in both function and scale to NSTX. The availability of a technically diverse and experienced staff provides confidence that the WBS5 work elements will effectively support the NCSX project's CD-4 objectives.



